## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Johannes Becker

Serial No.:

10/706,365

Filed:

November 12, 2003

Title:

SYSTEM AND METHOD FOR SECURING AN INTEGRATED

CIRCUIT AS AGAINST SUBSEQUENT REPROGRAMMING

Grp./A.U.:

2435

Examiner:

Suman Debnath

Confirmation No.:

6816

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Thereby certify that this correspondence is being electronically flexivity function Sales Faint and frademark Office on:

April 6, 2010 (Date)

Janua R. Williford

(Gained or byted name of person signing the certificate)

[Janua R. Williford/]

(Signature of the person signing the certificate)

Sir:

## REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.111

The Applicant has carefully considered this application in connection with the non-final Examiner's Action electronically delivered January 7, 2010 (hereinafter "Office Action"), and respectfully requests reconsideration of this application in view of the following remarks.